

Notice of Allowability	Application No.	Applicant(s)	
	10/803,338	KAVIANI, ALIREZA S.	
	Examiner	Art Unit	
	My-Trang N. Ton	2816	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☐ This communication is responsive to _____.
2. ☒ The allowed claim(s) is/are 1-21.
3. ☒ The drawings filed on 17 March 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|---|
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449, or PTO/SB/08),
Paper No./Mail Date <u>3/17/04, 6/14/04</u> 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input checked="" type="checkbox"/> Other <u>IDS 11/15/04</u> |
|--|---|

Not Available Copy



**MY-TRANG NUTON
PRIMARY EXAMINER**

Reasons for allowance

The following is an examiner's statement of reasons for allowance:

The present invention is drawn to an oscillator controller in an integrated circuit utilizing differential logic, a clock signal, an oscillator signal, a control logic and control signals or first means and second means recited in claims 1-21. None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: "control logic for receiving control signals ... the three states including a first state for propagating by the differential logic an edge of the clock signal to the differential output for a hard-phase alignment" in combination with "differential logic for receiving a differential clock input and a differential oscillator input ... having different frequencies " as recited in claim 1; "control logic for receiving control signals ... and in the hard-phase alignment state responsive to the control signals" in combination with "differential logic for receiving a differential clock input and a differential oscillator input ... having different frequencies" and "the differential logic configured to provide a combinational circuit in an oscillator alignment state and to provide a sequential circuit in a hard-phase alignment state" as recited in claim 13; the combination of "first means for receiving a differential clock input and a differential oscillator input ... having different frequencies", "the first means configured to provide a combinational circuit in an oscillator alignment state and to provide a sequential circuit in a hard-phase alignment state" and "second means ... and in the hard-phase alignment state responsive to the control means" as recited in claim 20; the combination of "first means ... at least one of the clock signal and the oscillator signal" and "second

means ... a first state for propagating by the first means an edge of the clock signal to the differential output for a hard-phase alignment” as recited in claim 21.

The prior art references submitted by the applicant on IDS forms received on 03/17/04 & 06/14/04 & 11/15/04 were reviewed and considered. These references appear to be the best references with respect to the claimed invention and closely matched most of the references found during the examiner’s searches. However, similarly as noted above, the combination of “the differential logic” and “the control logic” as recited in claims 1 and 13 and the combination of “the first means” and “the second means” as recited in claims 20-21 are not disclosed. Therefore, the claims are patentably distinct over all these prior art references of record.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



My-Trang N. Ton
Primary Examiner
Art Unit 2816

February 14, 2005